

10/618 206

Cofc



PATENT
Attorney's Doc. No. 4591-332

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent No.: US 6,888,733 B2
Inventor: Jang et al.
Issued: July 9, 2003

For: MULTIPLE CHIP SYSTEM INCLUDING A PLURALITY OF NON-VOLATILE SEMICONDUCTOR MEMORY DEVICES

Ser. No.: 10/618,206
Group Art Unit: 2818

Filed: July 9, 2003
Examiner: Gene N. Auduong

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

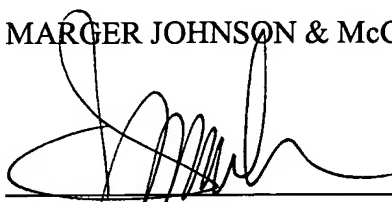
Certificate
JUL 27 2006
of Correction

Enclosed for filing are the following:

- ☒ Request for Certificate of Correction
- ☒ Certificate of Correction Form PTO 1050 (in duplicate)
- ☒ Return postcard
- ☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Respectfully submitted,


MARGER JOHNSON & McCOLLOM, P.C.


Gracie G. Cowger
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Date: July 18, 2006


Zach Cooper

JUL 28 2006



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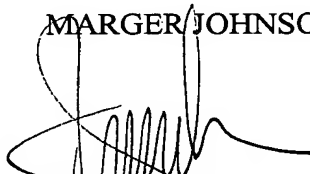
Upon comparison of the above-identified original Letters Patent with applicant's copy of the application therefore, the following error in the original Letters Patent has been noted:

At column 1, line 53, please replace "BUSY_, BUSY" with --BUSY_10, BUSY--
At column 4, line 40, please replace "status signals" with --status signal--

Applicant respectfully requests that a Certificate of Correction calling attention to the above error be issued pursuant to Rule 322 of the Patent Office and Title 35, United States Code, Section 254. Two copies of a Certificate of Correction form are enclosed.

Respectfully submitted,

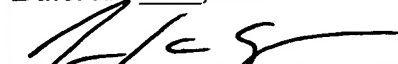
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(Also Form PTO-1050)

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DATED : July 9, 2003

INVENTOR(S) : Jang et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

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MAILING ADDRESS OF SENDER:
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Portland, Oregon 97204

PATENT NO. US 6,888,733 B2

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